AI accelerators for reconfigurable technologies

J. Bautista Roldán Aranda (jroldan@ugr.es), F. Jiménez Molinos (jmolinos@ugr.es), A.M. Cantudo Gómez (cantudo@ugr.es), and M. Villena Sánchez (mavillena@ugr.es)

Department of Electronics and Computer Technology
CITIC-University of Granada, Granada, Spain.

Calendar

Date	Morning session		Afternoon session	
	Responsible (Schedule)	Unit	Responsible (Schedule)	Unit
Monday 20/10	J. Bautista (10h-11h)	Concept of memristor. Axiomatic description by Leon Chua		
	J. Bautista (11h30-13h30)	Types of memristive memories		
Tuesday 21/10	J. Bautista (10h-12h)	Resistive switching and RRAMs: physical principles and types		

Calendar

Date	Morning session		Afternoon session	
	Responsible (Schedule)	Unit	Responsible (Schedule)	Unit
Wednesday 22/10	J. Bautista (10h-12h)	Simulators and types of simulations		
Thursday 23/10	F. Jiménez (10h-12h)	RRAM modeling		
Friday 24/10	F. Jiménez (10h-12h)	Von Neumann bottleneck. Neuromorphic computing (in-memory computing)		

Calendar

	Morning session		Afternoon session	
Date	**Responsible (Schedule)**	**Unit**	**Responsible (Schedule)**	**Unit**
Monday 27/10	F. Jiménez (10h-11h)	Crossbar arrays and selector devices		
	F. Jiménez (11h-12h & 12h30-13h)	Memristor-based circuits. Hardware neural networks		
Tuesday 28/10	A.M. Cantudo (10h-13h)	I-V measurement and parameter extraction		

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	Morning session		Afternoon session	
Date	**Responsible (Schedule)**	**Unit**	**Responsible (Schedule)**	**Unit**
Wednesday 29/10	A.M. Cantudo (10h-13h30)	Simulation using the Stanford model and statistical methods for variability analysis		
Thursday 30/10	M.A. Villena (16h-18h)	2D material memristors		