

Integrated Circuit Design with Open-Source Tools

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Calendar

Morning session			Afternoon session	
Date	Responsible (Schedule)	Unit	Responsible/ (Schedule)	Unit
Monday 14/07	F. Gámiz (9:30-12:30)	Introduction to ICs design		
Tuesday 15/07	C. Navarro (9:00-13:00)	Setting-up open source design tools		
Wednesday 16/07	F. Gámiz (10:00-14:00)	Analog Flow I: Xschem, GAW3 and NGSpice	F. Gámiz (15:30-18:30)	Analog Flow III: Xschem
Thursday 17/07	F. Gámiz (9:30-12:30)	Analog Flow II: Magic Layout, KLayout and DRC	F. Gámiz (15:30-18:30)	Analog Project I:

Calendar

Morning session			Afternoon session	
Date	Responsible (Schedule)	Unit	Responsible/ (Schedule)	Unit
Friday 18/07	F. Gámiz (09:30-12:00)	Analog Project II		
Monday 21/07	C. Navarro (9:00-13:00)	Digital Flow I: Building digital designs & verification	C. Navarro (15:30-18:30)	Digital Flow II: Verification & Openlane
Tuesday 22/07	C. Navarro (9:00-10:30)	Digital Flow III: Openlane & GDS generation	C. Navarro (15:30-19:00)	Digital Project II
	C. Navarro (10:30-13:00)	Digital Project I		

Calendar

- ☐ Analog and digital Flow sessions will be guided tutorials of the open-source design tools.
- ☐ During project sessions students Will work on example exercises.
- ☐ A laptop is recommended.