

Nanoelectronic Technologies: CMOS and New Materials

C. Márquez (carlosmgm@ugr.es), J. Ávila (jorgeavila@ugr.es) and J. Galdón (jcgaldon@ugr.es)

Nanoelectronics Research Group, Department of Electronics, CITIC-University of Granada, Granada, Spain.



Calendar

Morning session			Afternoon session	
Date	Responsible (Schedule)	Unit	Responsible/ (Schedule)	Unit
Monday 09/06	C. Márquez (9:30-14:00)	More than Moore + CMOS fabrication	All (15:30-18:30)	Laboratory Session 1
Tuesday 10/06	J. Galdón (9:00-12:00)	Deposition and patterning techniques	J. Ávila (15:30-18:30)	Optoelectronics applications
	J. Ávila (12:00-14:00)	Thin films deposition for electronics		
Wednesday 11/06	All (10:00-13:00)	Laboratory Session 2	All (15:00-18:00)	Laboratory Session 3

Calendar

Morning session			Afternoon session	
Date	Responsible (Schedule)	Unit	Responsible/ (Schedule)	Unit
Thursday 12/06	All (10:00-13:00)	Laboratory Session 4	All (15:30-18:30)	Laboratory Session 5
Friday 13/06	All (10:00-13:00)	Laboratory Session 6		
Monday 17/06	C. Márquez (10:00-13:00)	Laboratory Session 7		
Tuesday 18/06	J. Ávila (10:00-14:00)	Seminars		

Calendar

During laboratory sessions #1-6, students will be split in groups:

- ☐ Laboratory session 1, 3 & 5: Evaporation and wet etching + CVD Graphene growth and transfer+ PVD thin-film deposition and Sputtering.
- ☐ Laboratory session 2, 4 & 6: Lithography for etching (RIE) + CVD growth of TMDs and RAMAN + ALD thin-film deposition and EDX(S).
- ☐ Laboratory session 7 (all students): Sprocess simulations.